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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Watts et al.

App. No.: 10/757,778 GPAU: 1732

Filing Date: January 15, 2004 Examiner: Michael Colaianni

Dkt. No.: P80-22-03 Conf. No.: 6151

For: METHOD TO IMPROVE THE FLOW RATE OF IMPRINTING MATERIAL

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

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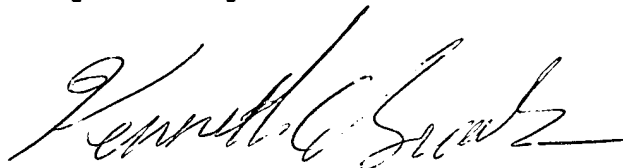
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

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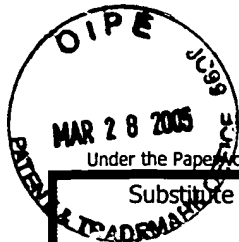
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Respectfully Submitted,



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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(use as many sheets as necessary)*

Sheet 1 of 6

Complete if Known

APPLICATION NUMBER	10/757,778
FILING DATE	01/15/2004
FIRST NAMED INVENTOR	Watts et al.
Group Art Unit	1732
Examiner Name	Colaianne, Michael
Attorney Docket Number	P80-22-03

U.S. PATENT DOCUMENTS

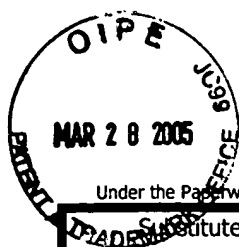
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	D1	5,425,848		Haisma et al.	6/20/1995	
	D2	6,309,580	B1	Chou	10/30/2001	
	D3	6,696,220	B2	Bailey et al.	2/24/2004	
	D4	2004/0007799	A1	Choi et al.	1/15/2004	
	D5	2004/0021254	A1	Sreenivasan et al.	2/5/2004	
	D6	2004/0021866	A1	Watts et al.	2/5/2004	
	D7	2004/0022888	A1	Sreenivasan et al.	2/5/2004	
	D8	2002/0177319	A1	Chou	11/28/2002	
	D9	6,713,238	B1	Chou et al.	3/30/2004	
	D10	2004/0008334	A1	Sreenivasan et al.	1/15/2004	
	D11	2004/0009673	A1	Sreenivasan et al.	1/15/2004	
	D12	2004/0124566	A1	Sreenivasan et al.	7/1/2004	
	D13	6,646,662	B1	Nebashi et al.	11/11/2003	
	D14	6,776,094	B1	Whitesides et al.	8/17/2004	
	D15	6,518,189	B1	Chou	2/11/2003	
	D16	2002/0132482	A1	Chou	9/19/2002	
	D17	2002/0167117	A1	Chou	11/14/2002	
	D18	6,482,742	B1	Chou	11/19/2002	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
Sheet 2 of 6				APPLICATION NUMBER 10/757,778	
				FILING DATE 01/15/2004	
				FIRST NAMED INVENTOR Watts et al.	
				Group Art Unit 1732	
				Examiner Name Colaianne, Michael	
				Attorney Docket Number P80-22-03	

U.S. PATENT DOCUMENTS						
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	D19	2003/0080471	A1	Chou	5/1/2003	
	D20	2004/0036201	A1	Chou et al.	2/26/2004	
	D21	2004/0192041	A1	Jeong et al.	9/30/2004	
	D22	6,828,244	B2	Chou	12/7/2004	
	D23	6,809,356	B2	Chou	10/26/2004	
	D24	2004/0197843	A1	Chou et al.	10/7/2004	
	D25	2004/0156108	A1	Chou et al.	8/12/2004	
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	D33	6,580,172	B2	Mancini et al.	6/17/2003	
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	D35	6,517,995	B1	Jacobenson et al.	2/11/2003	
	D36	2004/0110856	A1	Young et al.	6/10/2004	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 30%;">APPLICATION NUMBER</td> <td>10/757,778</td> </tr> <tr> <td>FILING DATE</td> <td>01/15/2004</td> </tr> <tr> <td>FIRST NAMED INVENTOR</td> <td>Watts et al.</td> </tr> <tr> <td>Group Art Unit</td> <td>1732</td> </tr> <tr> <td>Examiner Name</td> <td>Colaianne, Michael</td> </tr> <tr> <td>Attorney Docket Number</td> <td>P80-22-03</td> </tr> </table>		APPLICATION NUMBER	10/757,778	FILING DATE	01/15/2004	FIRST NAMED INVENTOR	Watts et al.	Group Art Unit	1732	Examiner Name	Colaianne, Michael	Attorney Docket Number	P80-22-03
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<i>(use as many sheets as necessary)</i>																	
Sheet	3	of	6														

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Sheet

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FOREIGN PATENT DOCUMENTS

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		Office 3	Number ⁴	Kind Code ⁵ (if known)				
	D41	WO	00/21689		Chou et al.	4/20/2000		
	D42	JP	02-92603		Kurikawa et al.	4/3/1990		
	D43	JP	02-24848		Kamio	1/26/1990		
	D44	WO	01/47003		Steiner et al.	6/28/2001		
	D45	WO	02/07199		Chou	1/24/2002		
	D46	WO	2004/114016		Chou et al.	12/29/2004		
	D47	WO	99/05724		Chou et al.	2/4/1999		
	D48	WO	03/010289		Chou et al.	2/6/2003		
	D49	WO	03/079416		Chou	9/25/2003		
	D50	WO	03/099536		Chou et al.	12/4/2003		

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Sheet 5 of 6				Attorney Docket Number	P80-22-03

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	D51	Choi et al., Design of Orientation Stages for Step and Flash Imprint Lithography, Precision Engineering, pp. 192 - 199, 1/1/2001	
	D52	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	D53	Johnson et al., Advances in Step and Flash Imprint Lithography, SPIE Microlithography Conference, 2/23/2003	
	D54	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	D55	Translation of Japanese Patent 02-92603	
	D56	Translation of Japanese Patent 02-24848	
	D57	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002	
	D58	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	D59	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	

Examiner Signature		Date Considered	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	D60	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	
	D61	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	
	D62	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	D63	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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